

### **IN THE CLAIMS**

Please amend the claims as follows:

1. – 41. (Canceled)

42. (New) An agree branch prediction apparatus, comprising:

at least a first branch history shift register to store correlated branch history information associated with an execution of a plurality of user instructions; and

at least a second branch history shift register to store correlated branch history information associated with an execution of a plurality of operating system instructions, wherein the first branch history shift register and the second branch history shift register are separated.

43. (New) The agree branch prediction apparatus of claim 42, further comprising:

a Gshare branch predictor comprising the first branch history shift register and the second branch history shift register.

44. (New) An agree branch prediction apparatus, comprising:

a Gshare branch predictor comprising a first branch history shift register to store correlated branch history information associated with a first operating context selected from a preselected plurality of operating contexts; and

a second branch history shift register included in the Gshare branch predictor, wherein the second branch history shift register is to store correlated branch history information associated with a second operating context selected from the preselected plurality of operating contexts.

45. (New) A multi-hybrid branch prediction apparatus, comprising:

at least a first branch history shift register to store correlated branch history information associated with an execution of a plurality of user instructions; and

at least a second branch history shift register to store correlated branch history information associated with an execution of a plurality of operating system instructions, wherein the first branch history shift register and the second branch history shift register are separated.

46. (New) The multi-hybrid branch prediction apparatus of claim 45, further comprising:

a Gshare branch predictor comprising the first branch history shift register and the second branch history shift register.

47. (New) A multi-hybrid branch prediction apparatus, comprising:

a Gshare branch predictor comprising a first branch history shift register to store correlated branch history information associated with a first operating context selected from a preselected plurality of operating contexts; and

a second branch history shift register included in the Gshare branch predictor, wherein the second branch history shift register is to store correlated branch history information associated with a second operating context selected from the preselected plurality of operating contexts.

48. (New) A bi-mode branch prediction apparatus, comprising:

at least a first branch history shift register to store correlated branch history information associated with an execution of a plurality of user instructions; and

at least a second branch history shift register to store correlated branch history information associated with an execution of a plurality of operating system instructions, wherein the first branch history shift register and the second branch history shift register are separated.

49. (New) The bi-mode branch prediction apparatus of claim 48, further comprising:

a Gshare branch predictor comprising the first branch history shift register and the second branch history shift register.

50. (New) A bi-mode branch prediction apparatus, comprising:

a Gshare branch predictor comprising a first branch history shift register to store correlated branch history information associated with a first operating context selected from a preselected plurality of operating contexts; and

a second branch history shift register included in the Gshare branch predictor, wherein the second branch history shift register is to store correlated branch history information associated with a second operating context selected from the preselected plurality of operating contexts.